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Sim, J.D.; Galbraith, F.S.; Davenport, N.;

Electromagnetic Compatibility, 1997. 10th International Conference on (Conf. Pu 445), 1-3 Sep 1997

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2 A CAD package for the prediction and reduction of radio-frequency interference from microprocessor based electronic equipment

Marvin, A.C.; Tealby, J.M.; Carr-Brion, T.J.F.; Porter, S.J.; Mann, S.M.;

Interference and Design for EMC in Microprocessor Based Systems, IEE Colloquium 14 Jun 1990

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3 HERO: Hierarchical EMC-constrained routing

Theune, D.; Thiele, R.; Lengauer, T.; Feldmann, A.;

Computer-Aided Design, 1992. ICCAD-92. Digest of Technical Papers., 1992 IEEE International Conference on, 8-12 Nov, 1992

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4 An algorithm for automated printed circuit board layout and routing evaluation